

R. A. HUGHES

Blue Line The inverter 3101 and diode 3110 models were tested and released to production. It will be necessary to modify the base R-C components on these units because we weren't conservative enough on our first design.

Components A specification has been written for a silicon diode which we might use to replace the Westinghouse 320 A diodes which we currently use. This specification requests manufacturers to put four diodes in one package. (If possible) Presumably they would ease our tough packaging requirements in units such as flip flops. The specifications also points out that no back voltage rating is required. (We may use someones rejected diodes.) We expect to pay less for this diode than we do for 320 A's.

We have ordered 1000-1N540 diodes from Ohmite Manufacturing Co. These are a replacement for the TIG Diodes and are guaranteed to meet our specifications. Price: \$0.17/1000, \$0.145/100,000. The present price of TIG Diodes is \$.18. Ohmite also will supply us with a replacement for our type 309 diode (price \$0.59) for a price of \$0.43.

New Designs The following production releases have been issued:

1202	Flip Flop	B00665
1403	Clock	B00229
3110	Diode	B00639
3601	Pulse Amplifier	B00642
4105	Inverter	B00675
4110	Diode	B00676
4104	Inverter	B00674
4103	Inverter	B00671

Oversights The modification of the 1209 flip-flop, (Installing 2N588's in the internal flip flop) didn't wor out to well. The flip flop was too sensitive and therefore would be noise sensitive. We are considering a special test for 2N588's in this socket to overcome this difficulty. The replacement transistor would still be a 2N393 in this circuit.


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B. Gurley

The construction of PDP-1 is going very well. The check out will begin next week. This is a few days ahead of schedule. The Flexo is beginning schedule, however. It won't be unpacked until this afternoon. It was supposed to be in about two weeks ago. The machine we have received is far more complicated than we require. I'll talk to Mr. Candavella of Frieder so that future machines will have only the stuff we need. The wiring documentation of PDP has been continually improved so that succeeding machines will be assembled much more quickly.

H. A. Anderson

PDP Programming

Several preliminary utility programs for PDP are essentially complete and are being issued as M notes.

The first of these M notes merely lists the octal equivalent of all of the instructions for PDP.

The fact that we do not have toggle switch storage means that we must have some simple programs and methods available for converting and loading programs into PDP.

The first program will convert one instruction at a time to the "read in tape format." It gets the instruction to be converted from the Test Word switches. This format is used during read-in-mode operation of PDP. The result is a punched paper tape.

The second program does the same thing except that it gets the instruction to be converted from the paper tape reader. It will interpret the flexo codes for the octal digits and assemble them and punch a new paper tape in the read in mode format.

Both of these are being written up as M notes.

PDP Sales

An amazing amount of interest is being shown in PDP from potential customers considering the fact that no mention in news releases, mailings or literature has been made as yet.

Three rather promising leads for action in the next 2 to 3 months are being followed up. A quotation is in effect on one of these at the moment.

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H. E. Anderson (Cont)

3000 Series Literature

The text has been completed and is starting in the type setting phase. Prices and loading numbers still need to be dropped into place. We are aiming at having it ready by NEREM (November 17).

PDP Literature

The only folder to be prepared has really not been started as yet. The text will be simple and quite general. (Instruction code will probably not be included for example.) Photos are available for this, of course, from those taken for Datamation magazine.

M. Sandler

Statues of Finshed Products

	<u>ON HAND</u>	<u>ON ORDER</u>	<u>AVAILABLE</u>	<u>IN-PROCESS</u>
Test Equipment	562		562	550
System Equipment	830	68	762	530
Blue Line				40

Units to Stock: to October 29 645

Several of our assembly personnel have shown decided aptitude for doing the logic wiring for our systems. Expanded drawings provided by Engineering have helped us.

We have started tabulation of operation labor hours and costs. With a bit more history we will be able to more accurately estimate costs of new products and schedule production time.

L. Prentice

Rework on PDP-1

The Display Console is proceeding satisfactorily.

Two additional Mounting Racks have been ordered for PDP-1. A sub panel for Flexowriter circuits will be made up to-day.

The display chassis is in the design stage and should be completed during the coming week.

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L. Prentice (Cont)

There seems to be a good possibility of obtaining a machinist in the near future. This should be a substancial help.

A new paint drying frame is being fabricated in the shop.

E. Harwood

The PDP-1 wiring is proceeding on schedule. We have finished all the control wiring and are in the final stages of the pulse wiring.

All available plug-in units have been plugged in, and we expect the 1607 Pulse Amplifiers next week.

We are going to start the check out next week and have wired in temporary indicators and a toggle switch panel to use until the console is ready.

J. Fadiman

Since the last report on October 2, a new Memory Tester, MT-1512, has been constructed for Telemeter Magnetics (Los Angeles, Calif.) All wiring and construction has been completed with the exception of a few minor details such as installing the cooling fans, and some final lacing. All plug-in units are installed, with the exception of the Read-Write Switches 1971 which will be installed on Tuesday. (These were delayed for lack of Amperex 2N284A transistors; selected Philco 2N670 transistors are being used instead.) Final check-out will take place next week, and the machine will be ready for shipping on Monday, November 9th. Total time for construction and check-out: 6 weeks.

We are about to start work on another Memory Tester, MT-1514. Customer unknown. AMP and Burroughs are both interested, as well as the previously interested customers mentioned in the bi-weekly of Sept. 11.

We followed up our bid to ITT Labs for two Memory Exercisers with a series of sketches and text explaining them. We should hear definitely from ITT next week.

COMPANY CONFIDENTIAL

S. C. Olsen (Cont)

The second issue of "Didja Know" has been published, and the reaction to our new house organ continues very favorable. However, it is a job to dig up all the news that should be included. If you think of anything you consider newsworthy-- either as a single item or as a regular feature -- please tell me about it. All contributions gratefully accepted.

W. E. Weeton

During the month of October many potential customers were contacted in an effort to increase our sales. While this had little if any effect on the October sales it should effect November to some extent. Several point of interest are RCA in Camden, Philco Corporation in Philadelphia and American Bosch Arma in Long Island are all interested in Memory Testers and would like to see the Telemetering Magnetics unit. RCA is primarily interested in a 128 x 128 coincident current memory tester. Philco on a Word Address Memory Tester and American Bosch Arma in a Transfluxor Memory Tester. This has implications of both word Address and Coincident Current Tester. Sylvania, RCA, And Western Electric, and L.F.E. are all expected to buy test equipment or building blocks this month. In addition to these there are many other companies which are likely to buy during the months of October and November.

Several points of interest are I was able to visit the Control Data Corporation in Minneapolis, Minnesota for an hour or so and talk to several people there. They have expanded considerably taking over about 2/3 of the building which they started in. They have five computers that are in various stages of completion, one of them being nearly completed having only a small portion of the debugging to finish. Their computer is a 32,000 word system, 48 bits deep. It is broken down into two memories which are 16,000 words each and these in turn are broken down into stacks that are 24 bits deep each. The basic cycle time for the memory is 6.4 microseconds, however, they are able to call on one memory and then call on the other one in their programming and by interlacing these two calls can effectively increase the cycle of the memory to 3.2 microseconds. All of their logic is done with modular cards which are roughly two inches square FF91 cards and have on them small quantities.

